

# Center for VLSI Design

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Established center in 1991. A new technology called Path Programmable Logic (PPL) is an innovative design approach for compact integrated circuits. This technology has been developed in response to an identified marked niche for a less expensive, more user-friendly design program. The ability to interface PPL with other commercial software packages makes this design program attractive to smaller engineering firms needing custom integrated circuits.

Overview	Technologies	Status	Economic Impact
<p>Current State Contract \$100,000</p> <p>FY92 Matching Funds \$819,000</p> <p>Cumulative ..... \$1,065,000</p> <p>Total Jobs Created ..... 71</p> <p>Industry ..... 53</p> <p>Center ..... 18</p> <p>Direct Center Spin-offs ..... 1</p> <p>Total Benefiting Utah Companies 5</p> <p>License Agreements ..... 1</p> <p>Patents Applied ..... 0</p> <p>Patents Issued ..... 0</p>	<p>* Research and development of tools for design of integrated circuits</p> <p>* Physical placement of logic software, libraries and cell libraries</p> <p>* Interfaces to 3rd party software</p>	<p>* Transfer of source code of licensed technology to private Utah company</p> <p>* Development of new tools and libraries in CMOS, GaAs and HBT technologies</p> <p>* New graphical user interface under development</p> <p>* Presently writing proposals to NSF and DARPA</p>	<p>* Joint venture was formed with a European company to sell center products in Europe</p> <p>* First generation sales of software beginning</p> <p>* Beginning discussions with companies in the Pacific Rim</p>

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